

**DEPARTMENT OF COMPUTER SCIENCE**

**COMPUTER ARCHITECTURE  
LAB REPORT**

**SUBMITTED BY:**

{name}

{roll}

3rd Semester - Section {section}

**SUBMITTED TO:**

Department of Computer Science

St. Xavier’s College

Maitighar, Kathmandu

ST. XAVIER’S COLLEGE

MAITIGHAR, KATHMANDU

COMPUTER ARCHITECTURE   
PRACTICAL INDEX SHEET

**B.Sc. CSIT 3rd Semester**

T.U. Regd. No.:………………………………… Class Roll No.:.…...................

Name of Student:……………………………….. Year/Sem.:...............………...

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **S.No.** | **Title of the Experiment** | **Final Submission Date** | **Signature** | **Remarks** |
| 1 | FAMILIARIZATION WITH VHDL PROGRAMMING |  |  |  |
| 2 | DESIGN HALF ADDER AND 2X4 DECODER USING VHDL |  |  |  |
| 3 | DESIGN CIRCUIT USING DATA FLOW STYLE AND BEHAVIORAL STYLE |  |  |  |
| 4 | COMPUTER ARITHMETIC |  |  |  |